

**INTRODUCTION TO DIGITAL SYSTEMS: MODELING,
SYNTHESIS, AND SIMULATION USING VHDL**

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Introduction to Digital Systems: Modeling, Synthesis, and Simulation Using VHDL introduces the application of modeling and synthesis in the effective design of digital systems and explains applicable analytical and computational methods. Through step-by-step explanations and numerous examples, the author equips readers with the tools needed to model, synthesize, and simulate digital and Simulation Using VHDL using Very High Speed Integrated Circuit Hardware Description Language VHDL programming.

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